



Amendments to the Specification:

Please replace the last paragraph on page 7 of the disclosure with the following amended paragraph:

AI --The amplification chain 1220, in an exemplary embodiment, includes three cascaded stages, realized for example using FET devices. The stages are drain modulated and driven in switch mode or, for low-power operation, in "multiplicative" mode, as described more particularly i[[s]]n U.S. Patent Application No.09/834,024 (~~Attorney's Dkt. No. 1103—LDM-US~~), entitled "Communications Signal Amplifiers Having Independent Power Control and Amplitude Modulation", filed on even day herewith and incorporated herein by reference. An RF input port 1221 of the amplification chain may be regarded as the phase port, and the drains (or power supply inputs) of the stages may be regarded together as the amplitude port 1223.--